What is claimed is:

1. An apparatus for securing a recording medium driver, which encrypts data provided from an interface port of a computer main board and inputs the encrypted data to an interface port of the recording medium driver, and decrypts the data provided from an interface port of the recording medium driver and inputs the decrypted data to the interface port of the computer main board, the apparatus comprising:

an encrypter for encrypting the data provided from the interface port of the computer main board using a logic circuit and inputting the encrypted data to the interface port of the recording medium driver;

a decrypter for decrypting the data provided from the interface port of the recording medium driver using a logic circuit and inputting the decrypted data to the interface port of the computer main board; and

a memory for receiving data to be used for encrypting from a user and providing the received data to the encrypter and the decrypter.

2. The apparatus of claim 1, further comprising;

a key input interface connected to the memory,

Wherein, when a user inserts a key of an EEPROM, where the data to be used for encrypting is saved, into the key input interface, the data to be used for encrypting is inputted to the memory.

3. A method for securing a recording medium driver by encrypting 16-bit data provided from an interface port of a computer main board and inputting the encrypted data to an interface port of the recording medium driver, the method comprising:

saving data to be used for encrypting in a memory in such a way that different data can be saved without any identical data;

reading 8-bit data of an address which has the same value as 4-bit data in 16-bit data provided from the interface port of the computer main board, from the memory; and

replacing 8-bit data of the 16-bit data provided from the interface port of the computer main board with the 8-bit data created as a result of a logic operation performed on 8-bit data of the 16-bit data provided from the interface port of the computer main board and the 8-bit data read from the memory.

4. The method of claim 3, wherein reading and replacing are performed by relevant logic circuits.

5. The method of claim 3, wherein the interface port can be either an Advanced Technology Attachment (ATA) port or a Small Computer System Interface (SCSI) port.